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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.
09/138,378	08/24/98	HAMURA S	1046.1188/JD

WM31/0314

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EXAMINER	
GARCIA, G	
ART UNIT	**PAPER NUMBER**
2624	7

DATE MAILED: 03/14/01

Please find below and/or attached an Office communication concerning this application or proceeding.

Commissioner of Patents and Trademarks

Office Action Summary

Application No.	Applicant(s)
09/138,378	Hamura et al.
Examiner	**Group Art Unit**
G. Garcia	2624

☒ Responsive to communication(s) filed on *Dec 19, 2000*

☒ This action is **FINAL.**

☐ Since this application is in condition for allowance except for formal matters, **prosecution as to the merits is closed** in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11; 453 O.G. 213.

A shortened statutory period for response to this action is set to expire *three* month(s), or thirty days, whichever is longer, from the mailing date of this communication. Failure to respond within the period for response will cause the application to become abandoned. (35 U.S.C. § 133). Extensions of time may be obtained under the provisions of 37 CFR 1.136(a).

Disposition of Claim

☒ Claim(s) *1-12* is/are pending in the applicat

Of the above, claim(s) _____ is/are withdrawn from consideration

☐ Claim(s) _____ is/are allowed.

☒ Claim(s) *1-12* is/are rejected.

☐ Claim(s) _____ is/are objected to.

☐ Claims _____ are subject to restriction or election requirement.

Application Papers

☐ See the attached Notice of Draftsperson's Patent Drawing Review, PTO-948.

☐ The drawing(s) filed on _____ is/are objected to by the Examiner.

☒ The proposed drawing correction, filed on *Dec 19, 2000* is ☒ approved ☐ disapproved.

☐ The specification is objected to by the Examiner.

☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. § 119

☐ Acknowledgement is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d).

☐ All ☐ Some* ☒ None of the CERTIFIED copies of the priority documents have been

☐ received.

☐ received in Application No. (Series Code/Serial Number) _____ .

☐ received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

*Certified copies not received: _____

☐ Acknowledgement is made of a claim for domestic priority under 35 U.S.C. § 119(e).

Attachment(s)

☐ Notice of References Cited, PTO-892

☐ Information Disclosure Statement(s), PTO-1449, Paper No(s). _____

☐ Interview Summary, PTO-413

☐ Notice of Draftsperson's Patent Drawing Review, PTO-948

☐ Notice of Informal Patent Application, PTO-152

--- SEE OFFICE ACTION ON THE FOLLOWING PAGES ---

DETAILED ACTION

1. This office action is in response to the amendment filed 12/19/00. The changed of address is acknowledged. Claims 1-12 are pending in this Application. Claims 1-10 have been amended, and claims 11 and 12 have been added. The objection to the abstract recited in the last office action is withdrawn in view of the new abstract submitted. In view of Applicant's amendments to the specification the rejection under 35 U.S.C. 112, first paragraph, is hereby withdrawn by the Examiner. In view of Applicant's amendment to the title the objection to the title is hereby withdrawn by the Examiner. In view of Applicant's amendments to the claims the objections to the claims recited in the last office action is hereby withdrawn by the Examiner.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

> A person shall be entitled to a patent unless --
>
> (e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371© of this title before the invention thereof by the applicant for patent.

3. Claims 1-12 are rejected under 35 U.S.C. 102(e) as being anticipated by Kageyama et al. (5,774,638).

With regard to claim 1, Kageyama et al. teaches a printer (figure 1, items 11 and 18) outputting a plurality of types of print data corresponding to one or more images to be printed on page (i.e. col. 5, lines 41-53 and col. 29, lines 4-11), each of the types of print data having an attribute (i.e.

col. 3, lines 10-24 and fig. 26), said printer comprising: an image buffer (i.e. figure 1, item 141 or 142) having a plurality of storage locations (i.e. figures 20-26, see also col. 27 and 28) and storing each type of prin, one by one, in a different one of storage locations according to the attribute of each type of print data (i.e. col. 3, line 10-20, col. 22, lines 62-67, and col. 23, lines 48-55, see also figure 26); a plurality of video interfaces (i.e. figure 1, items 104,114 and 124), each of said video interfaces independently reading each of the types of print data stored in a corresponding storage location of said image buffer (reads on figure 1, the interfaces (104,114 and 124) read the data from the shared memory (141) to be processed by the different image processors (107,117 and 127)); a print data integration circuit (reads on figure 1, item 100, which controls the integration of data to be printed by the print engine 18, see col. 3, line 55 thru col. 4, line 11) integrating the plurality of types of print data read by the video interfaces to be printed on one page (e.g. col. 5, lines 41-53 and col. 29, lines 4-11); and an output mechanism (figure 1, item 18) outputting the integrated print data on one page (i.e. col. 3, line 55 thru col. 4, line 11 and col. 5, lines 41-53).

With regard to claim 2, Kageyama et al. further teaches the plurality of types of print data stored in said image buffer contain form print data corresponding to a form and text print data corresponding to a text to be printed over the form (fig. 16 and col. 24, lines 7-40).

With regard to claim 3, Kageyama et al. further teaches a printer having separation unit (reads on fig. 1, item 100) for separating print data corresponding to an image with a text into a type of print data corresponding to the image and type of print data corresponding to the text (e.g. col. 3, line 10 thru col. 4, line 19); and a storage unit (fig. 1, item 141) for storing each of the

types of separated in said image buffer according to the attribute of each type of separated print data (e.g. col. 3, lines 10-32 and fig. 26).

With regard to claim 4, Kageyama et al. further teaches a printer comprising a plurality of image processing circuits (fig. 1, item 100,110, or 120), each of said image processing circuits applying an image process to the type of print data read by a corresponding one of said video interfaces (col. 5, lines 42-53).

With regard to claim 5, Kageyama et al. further teaches a plurality of types of print data stored in said image buffer are obtained by dividing print data corresponding to the image to be printed data on one page into a plurality of bands, and wherein said print data integration circuit alternately selects the print data read by each of said video interfaces and outputs the selected print data to said output mechanism (e.g. figures 20-24 and col. 5, lines 42-53). 5,774,638).

With regard to claims 6-12, the limitations of claims 6-12 are covered by the limitations of claims 2-5 above (e.g. part of the printer of claims 2-5 consist of the controller as claimed in claims 7-10).

Conclusion

4. Applicant's arguments filed 12/19/00 have been fully considered but they are not persuasive.

With regard to Applicant's argument that Kageyama does not teach a memory having a plurality of storage areas storing according to its attribute or type and that the memory appears to

correspond to a plurality of pages. Examiner respectfully disagrees with Applicant's conclusion. Examiner asserts that the memory of Kageyama teaches having a plurality of storage areas according to its attributes or type (i.e. figures 1, and 20-26). Figure 1 of Kageyama depicts a shared memory (141 or 142, i.e. col.27, lines 5-7) that can have a plurality of areas as described in figure 26, which depicts how a page can contain different subcommands that represent different types of images as shown in figures 20-23. In figures 20-23 the information is divided into different types or regions having different attributes parameters, these information is stored in memory according to a memory location and attribute as shown in figure 26. Figure 1, depicts how the master processor (100) integrates the different parts of the page by processing different parts of the page by different processors (i.e. such as 110 and 120), therefore enabling the system of Kageyama to independently process data by using the different processors and producing parallel processing as described in column 4, lines 21-29. With regard to Applicant's argument that Kageyama does not teach independently reading the types of data according to attribute of the data. Examiner respectfully disagrees with Applicant's conclusion. Examiner asserts that Kageyama perform different processes to different types of data and therefore reads the independently according to attribute (i.e. figure 26 and col. 4, lines 21-29 & col. 29, lines 4-11). e.g. figure 26 describes how the print data contains different attribute parameters having subcommands and col. 4, describes how the different types of data is processed independently by allowing the different processors to retrieve (or read) the data from the shared memory and therefore processing the data in parallel.

With regard to Applicant's argument that Kageyama does not teach a plurality of image processing circuits applying image process to the type of print data corresponding to the video interface. Examiner respectfully disagrees with Applicant's conclusion. Examiner asserts that figure 1, teaches the different processing circuits items 117 or 127 that process the print data and applies an image process to the print data by using items 118 and 128, corresponding to the data being processed by the video interfaces 114 and 124, the print data corresponds to print data assigned to the processor (114 or 124) to be processed as suggested by col. 4, lines 21-29.

5. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Gabriel I. Garcia whose telephone number is (703) 305-8751. The examiner can normally be reached Monday thru Thursday from 7:30AM-6:00PM.

Any inquiry of a general nature or relating to the status of this application should be directed to the Group receptionist whose telephone number is (703) 305-3900.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks
Washington, DC 20231

or faxed to:

(703) 306-5406 (official)
(703) 308-5397 (unofficial)

Gabriel Garcia

Gabriel I. Garcia
Patent Examiner
March 9, 2001